

## **Bernardo Petracchi**

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### **Academic Education**

Laurea Magistrale (MS), Electronic Engineering, University of Florence 2021  
Laurea (BS), Electronic and Telecommunications Engineering, University of Florence 2018

### **Other Education**

Introduction to Simulation and Debug of FPGAs, Intel 2021  
Introduction to FPGAs and the Intel® Quartus® Prime Software, Intel 2021  
Sommelier Certificate (second level), FISAR, Prato, Italy 2019  
Sommelier Certificate (first level), FISAR, Prato, Italy 2018

### **Scholarships**

Erasmus Scholarship, RWTH Aachen University 2018

### **Work Experience**

High school tutor (mathematics, physics, computer science, and electronics) 2016-2021  
Grape harvester, Piandaccoli Farm, Florence, Italy 2016

### **Skills and Competencies**

- English (fluent), Italian (native)
- MATLAB, Simulink, Intel Quartus Prime, VHDL, C, LabVIEW
- Communication skills
- Attention to detail
- Flexible and adaptive
- Time-management skills and punctuality
- Teamwork skills

### **Hobbies and Interests**

In my spare time, I enjoy playing soccer, cycling, reading, wine tasting, and cooking.