



University of Pavia

**Ph.D. School of Electrical and Electronics Engineering and Computer Science
Ph.D. School in Microelectronics**

SEMINAR

MEMS Overview: History and Future Trend

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Aula Seminari INFN - STMicroelectronics, Via Ferrata 1, Pavia

Abstract: In this lecture, the MEMS origin inside STMicroelectronics will be presented, with some anecdotes really happened. The evolution of different products in production and the economical results will be covered. The presentation will finish with a vision on the future of this exploding market segment.

Biography: Bruno Murari joined SGS, now STMicroelectronics, in 1961. He became head of linear IC design and development at the STM laboratories in Castelletto in 1972 and director of the Castelletto Site in 1981. He was responsible for all the activities of this center, which is dedicated to the development of technologies for Smart Power ICs, MEMS and Photonics. He has personally designed 10 integrated circuits and has supervised the design of more than 2000 others. He holds more than 80 patents concerning IC design and technology and has published more than 40 papers. In 1994 he got the Maestro del Lavoro honor by the President of the Italian Republic. He received Honorary degrees in Information Science from the Ca' Foscari University of Venice for his role in the development of Smart Power IC technologies in 1995 and in Electronic Engineering from the Politecnico of Milan for his fundamental innovations in semiconductor technologies and IC design in 2002. In 2011 he was winner of the Sapio prize for Industry for his contribution to integrated circuits. He retired in 2005 but maintains the role of scientific advisor in STM.

Organizer

Prof. Sabina Merlo

Ph.D. Coordinators

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